



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/054,653 Confirmation No.: 9448  
First Named Inventor: Constantin Bulucea Filing Date: January 18, 2002  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127 US  
Title: Gate-Enhanced Junction Varactor With Gradual Capacitance Variation  
Assignee: National Semiconductor Corporation

San Jose, California  
23 January 2003

COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Restriction Requirement mailed December 26, 2002, Applicant's attorney provisionally elects Claims 1-60 in Group I.

Please telephone applicant's attorney at 408-453-9200, ext. 1371, if there are any questions.

EXPRESS MAIL LABEL NO:

EV 240 085 490 US

Respectfully submitted,

*Ronald J. Meetin*

Ronald J. Meetin  
Attorney for Applicants  
Reg. No. 29,089

LAW OFFICES OF SKJERVEN MORRILL LLP  
25 Metro Drive, Suite 700  
San Jose, CA 95110

LAW OFFICES OF  
SKJERVEN MORRILL LLP  
San Jose, CA  
San Francisco, CA

RECEIVED  
JAN 28 2003  
TECHNOLOGY CENTER 2800